

No	Terminal Name	I/O	Description	P.OFF	P.Failure	Reset/Release
61	PAL 9H(H)	O	PAL 9H: High Except PAL 9H: Low	Low	Low	Low
62	VCR/TV	O	Control signal for TV/VCR output selection. * "Low" output is set at shift to P.OFF or T. standby. * "High" output is set at shift to VV from EE. * The output is inverted whenever TV/VCR key is received.	Low	Low	Low
63	VBI2	O	NAVI writing terminal. * During NAVI data being written (During recording when JET NAVIGATOR is ON). OSD letters (Including the masking) being written : High Except OSD letters being written : Low * Other than above: Hi-z	Low / Hi-Z	Low / Hi-Z	Low / Hi-Z
64	VBI1/CHARA	O	Terminal for both NAVI Writing and OSD REC Writing. 1. During NAVI writing (During recording in JET NAVIGATOR is ON) At NAVI data "H" being written : High is output At NAVI data "L" being written : Low is output At except NAVI data being written : Low is output 2. During OSD REC (During recording in OSD REC is ON) During OSD letters (Except the masking) being written : High is output Except OSD letters being written : Low * Other than above condition : Hi-z	Low / Hi-Z	Low / Hi-Z	Low / Hi-Z
65	FLD_CS	O	Chip select signal for FIP driver. *Active: "Low" *Non-active: "High"	(Normal ope.)	Low	Low
66	UNLOADING(H)	O	Control terminal for the Unloading operation.	Low	Low	Low
67	LOADING(H)	O	Control terminal for the loading operation.	Low	Low	Low
68	IC.DATA.OUT	O	Timer-Bus signal for peripheral ICs control: Data output	(Normal ope.)	In	Hi-Z
69	IC.DATA.IN	I	Timer-Bus signal for peripheral ICs control: Data input	(Normal ope.)	In	In
70	IC.DATA.CLK	O	Timer-Bus signal for peripheral ICs control:Clock outout	(Normal ope.)	In	Hi-Z
71	IIC.CLK	O	Serial communication terminal (IIC) for IC3001/FM audio IC.	(Normal ope.)	In	Hi-Z
72	IIC.DATA	I/O	Serial communication terminal (IIC) for IC3001/FM audio IC.	(Normal ope.)	In	Hi-Z
73	125Hz/ROM.CORE	O	ROM Correction confirmation mode: * ROM correction setting bit is "ON": "High". * ROM correction setting bit is "OFF": "Low" is existed. (Other than ROM Correction confirmation mode, this terminal is the output terminal of internal clock for main clock adjustment.:Outputting the 125Hz.)	(Normal ope.)	Low	Low
74	CAP R/F	O	The rotation direction control terminal of the capstan driver. *RVS="High" *FWD="Low".	Low	Low	Low
75	NC	O	Low fix.	Low	Low	Low
76	CAP.ET	O	Power supply terminal for the capstan motor control. (Compared with the driver reference voltage, when it is "low", current will be cut. Also when it is "high", the rotation speed will be accelerated.)	High PWM=0V	Low	High PWM=0V
77	CYL.ET	O	Power supply terminal of the cylinder motor control: (Compared with the driver reference voltage, when it is "high", current will be cut. Also when it is "low", the rotation speed will be accelerated. (Max:2.8V))	Low PWM=2.800V	Low	Low PWM=2.800V
78	P.FAIL(L)	I	Input terminal for the power failar detection. Power failar : "Low".	In	In	In
79	S.REEL.PULSE	I	Input terminal of the S.Reel pulse.	In	In	In
80	T.REEL.PULSE	I	Input terminal of the T.Reel pulse.	In	In	In
81	BIAS(L)	O	Linear Audio REC/ERASE ON/OFF control terminal. (FULL ERASE ON/OFF control is combined with) *When recording the linear audio, the "Low" is output synchronizing with D.REC signal for IC3001. Output mode: REC(ADUB/AV-INSERT) *When the recording starts, "Low" is output at 140-160m sec. after D.REC (H) for IC3001 has been shifted to "High" from "Low". *When the recording stops, the "High" is output at 0-20m sec. after D.REC for IC3001 has being shifted to "low" from "High".	In	In	In